

Yearly Syllabus for Undergraduates
As recommended by Central Board of Studies of Electronics
For approval of Kuladhipati, Governor of Chhattisgarh

Session 2018-19
July 2018 onwards
Class: B.Sc. Electronics

Scheme of Examination

Paper Code	Course Opted	Title of Course	Theory	Practical	Grand Total	Minimum Passing Marks
First Year						
ELB-101	Core Course	Network Analysis And Analog Electronics	50		100	33
ELB-102	Core Course	Linear and Digital Integrated Circuits	50			
ELB-103P	Core Course Practical/Tutorial	Networks Analysis and Analog Electronics Lab	25	50	50	17
ELB-104P	Core Course Practical/Tutorial	Linear and Digital Integrated Circuits Lab	25			

Syllabus

B. Sc. Part I ELECTRONICS

Paper-I

ELB-101: NETWORK ANALYSIS AND ANALOG ELECTRONICS
Theory: Maximum Marks 50

Unit-1

Basic Circuit Concepts: Voltage and Current Sources, Review of Resistors, Inductors, Capacitors. Circuit Analysis: Kirchoff's Current Law (KCL), Kirchoff's Voltage Law (KVL),
AC Circuit Analysis: Sinusoidal Voltage and Current, Definition of Instantaneous, Peak, Peak to Peak, Root Mean Square and Average Values. AC applied to Series RC and RL circuits: Impedance of series RC & RL circuits. AC applied to Series and parallel RLC circuit, Series and Parallel Resonance, condition for Resonance, Resonant Frequency, Bandwidth, and significance of Quality Factor (Q).

Passive Filters: Low Pass, High Pass.

Network Theorems: Principal of Duality, Superposition Theorem, Thevenin's Theorem, Norton's Theorem, Reciprocity Theorem, Millman's Theorem, Maximum Power Transfer Theorem. AC circuit analysis using Network theorems.

Unit-2

Junction Diode and its applications: PN junction diode (Ideal and practical)-constructions, Formation of Depletion Layer, Diode Equation and I-V characteristics. Idea of static and dynamic resistance, dc load line analysis, Quiescent (Q) point. Zener diode, Reverse saturation current, Zener and avalanche breakdown. Rectifiers- Half wave rectifier, Full wave rectifiers (center tapped and bridge), circuit diagrams, working and waveforms, ripple factor and efficiency. Filter-Shunt capacitor filter, its role in power supply, output waveform, and working. Regulation- Line and load regulation, Zener diode as voltage regulator, and explanation for load and line regulation.

Unit-3

Bipolar Junction Transistor: CE, CB Characteristics and regions of operation, Transistor biasing, DC load line, operating point, thermal runaway, idea about stability and stability factor. Voltage divider bias, circuit diagrams and their working.

Field Effect Transistors: JFET, Construction, Working and Characteristics. MOSFET, Construction, Working and Characteristics.

Power Devices: UJT, Construction, Working and Characteristics. SCR, Diac, Triac, Construction, Working and Characteristics and Applications.

Unit-4

Amplifiers: Transistor biasing and Stabilization circuits- Fixed Bias and Voltage Divider Bias. Thermal runaway, stability and stability factor S. Transistor as a two port network, h-parameter equivalent circuit. Small signal analysis of single stage CE amplifier. Input and Output impedance, Current and Voltage gains. Class A, B and C Amplifiers.

Cascaded Amplifiers: Two stage RC Coupled Amplifier and its Frequency Response.

Unit-5

Feedback in Amplifiers: Concept of feedback, negative and positive feedback, advantages of negative feedback (Qualitative only).

Sinusoidal Oscillators: Barkhausen criterion for sustained oscillations. Phase shift, Wein's bridge, Crystal and Colpitt's oscillator. Determination of Frequency and Condition of oscillation.

Reference Books:

- [1] Electric Circuits, S. A. Nasar, Schaum's outline series, Tata McGraw Hill (2004)
- [2] Electrical Circuits, M. Nahvi & J. Edminister, Schaum's Outline Series, Tata McGraw-Hill (2005)
- [3] Electrical Circuits, K.A. Smith and R.E. Alley, 2014, Cambridge University Press
- [4] Network, Lines and Fields, J.D.Ryder, Prentice Hall of India.
- [5] Electronic Devices and Circuits, David A. Bell, 5th Edition 2015, Oxford University Press.
- [6] Electronic Circuits: Discrete and Integrated, D.J. Schilling and C. Belove, Tata McGraw Hill
- [7] Electrical Circuit Analysis, Mahadevan and Chitra, PHI Learning
- [8] Microelectronic circuits, A.S. Sedra, K.C. Smith, A.N. Chandorkar, 2014, 6th Edn., Oxford University Press.
- [9] J. Millman and C. C. Halkias, Integrated Electronics, Tata McGraw Hill (2001)
- [10] J. J. Cathey, 2000 Solved Problems in Electronics, Schaum's outline Series, Tata McGraw Hill (1991)

Paper- II

ELB-102: LINEAR AND DIGITAL INTEGRATED CIRCUITS

Theory:

Maximum Marks 50

Unit-1

Operational Amplifiers (Black box approach): Characteristics of an Ideal and Practical Operational Amplifier (IC 741), Open and closed loop configuration, Frequency Response, CMRR, Slew Rate and concept of Virtual Ground.

Applications of Op-Amps: (1) Inverting and non-inverting amplifiers, (2) Summing and Difference Amplifier, (3) Differentiator, (4) Integrator, (5) Wein bridge oscillator, (6) Comparator and Zero-crossing detector, and (7) Active low pass and high pass, Butterworth filter (1st order only).

Unit-2

Number System and Codes: Decimal, Binary, Octal and Hexadecimal number systems, base conversions. Representation of signed and unsigned numbers, BCD code. Binary, octal and hexadecimal arithmetic; addition, subtraction by 2's complement method, multiplication.

Logic Gates and Boolean algebra: Truth Tables of OR, AND, NOT, NOR, NAND, XOR, XNOR, Universal Gates, Basic postulates and fundamental theorems of Boolean algebra.

Unit-3

Combinational Logic Analysis and Design: Standard representation of logic functions (SOP and POS), Minimization Techniques (Karnaugh map minimization up to 4 variables for SOP). Arithmetic Circuits: Binary Addition. Half and Full Adder. Half and Full Subtractor, 4-bit binary Adder/Subtractor.

Data processing circuits: Multiplexers, De-multiplexers, Decoders, Encoders. Clock and Timer (IC 555): Introduction, Block diagram of IC 555, Astable and Monostable multivibrator circuits.

Unit-4

Sequential Circuits: SR, D, and JK Flip-Flops. Clocked (Level and Edge Triggered) Flip-Flops. Preset and Clear operations. Race-around conditions in JK Flip-Flop. Master-slave JK Flip-Flop.

Shift registers: Serial-in-Serial-out, Serial-in-Parallel-out, Parallel-in-Serial-out and Parallel-in-Parallel-out Shift Registers (only up to 4 bits).

Counters (4 bits): Ring Counter. Asynchronous counters, Decade Counter Synchronous Counter.

Unit-5

D-A and A-D Conversion: 4 bit binary weighted and R-2R D-A converters, circuit and working, Accuracy and Resolution. A-D conversion characteristics, successive approximation ADC. (Mention of relevant ICs for all).

Reference Books:

- [1] OP-Amps and Linear Integrated Circuit, R. A. Gayakwad, 4th edition, 2000, Prentice Hall
 - [2] Operational Amplifiers and Linear ICs, David A. Bell, 3rd Edition, 2011, Oxford University Press.
 - [3] Digital Principles and Applications, A.P. Malvino, D.P. Each and Saha, 7th Ed., 2011, Tata McGraw
 - [4] Fundamentals of Digital Circuits, Anand Kumar, 2nd Edn, 2009, PHI Learning Pvt. Ltd.
 - [5] Digital Circuits and systems, Venugopal, 2011, Tata McGraw Hill.
 - [6] Digital Systems: Principles & Applications, R.J.Tocci, N.S.Widmer, 2001, PHI Learning.
 - [7] Thomas L. Floyd, Digital Fundamentals, Pearson Education Asia (1994)
 - [8] R. L. Tokheim, Digital Principles, Schaunn's Outline Series, Tata McGraw- Hill (1994)
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ELECTRONICS LABORATORY
ELB 103P: NETWORK ANALYSIS AND ANALOG ELECTRONICS LAB
(Hardware and Circuit Simulation Software) **Max.Marks:25**

The scheme of practical examination will be as follows-

Experiment	--	30
Viva	--	10
Sessional	--	10
Total	--	50

AT LEAST 06 EXPERIMENTS FROM THE FOLLOWING BESIDES #1

1. To familiarize with basic electronic components (R, C, L, diodes, transistors), digital Multimeter, Function Generator and Oscilloscope.
2. Measurement of Amplitude, Frequency & Phase difference using Oscilloscope.
3. Verification of (a) Thevenin's theorem and (b) Norton's theorem.
4. Verification of (a) Superposition Theorem and (b) Reciprocity Theorem.
5. Verification of the Maximum Power Transfer Theorem.
6. Study of the I-V Characteristics of (a) p-n junction Diode, and (b) Zener diode.
7. Study of (a) Half wave rectifier and (b) Full wave rectifier (FWR).
8. Study the effect of (a) C- filter and (b) Zener regulator on the output of FWR.
9. Study of the I-V Characteristics of UJT and design relaxation oscillator..
10. Study of the output and transfer I-V characteristics of common source JFET.
11. Study of Fixed Bias and Voltage divider bias configuration for CE transistor.
12. Design of a Single Stage CE amplifier of given gain.
13. Study of the RC Phase Shift Oscillator.
14. Study the Colpitt's oscillator.

Reference Books:

1. Electrical Circuits, M. Nahvi and J. Edminister, Schaum's Outline Series, Tata McGraw-Hill (2005)
2. Networks, Lines and Fields, J.D.Ryder, Prentice Hall of India.
3. J. Millman and C. C. Halkias, Integrated Electronics, Tata McGraw Hill (2001)
4. Allen Mottershead, Electronic Devices and Circuits, Goodyear Publishing Corporation.

ELECTRONICS LAB

ELB 104P: LINEAR AND DIGITAL INTEGRATED CIRCUITS LAB

Max.Marks:25

At least 04 experiments each from section A, B and C

Section-A: Op-Amp. Circuits (Hardware)

1. To design an inverting amplifier using Op-amp (741,351)for dc voltage of given gain
2. (a) To design inverting amplifier using Op-amp (741,351) & study its frequency response
(b) To design non-inverting amplifier using Op-amp (741,351) & study frequency response
3. (a) To add two dc voltages using Op-amp in inverting and non-inverting mode
(b) To study the zero-crossing detector and comparator.
4. To design a precision Differential amplifier of given I/O specification using Op-amp.
5. To investigate the use of an op-amp as an Integrator.
6. To investigate the use of an op-amp as a Differentiator.
7. To design a Wien bridge oscillator for given frequency using an op-amp.
8. To design a circuit to simulate the solution of simultaneous equation and 1st/2nd order differential equation.
9. Design a Butterworth Low Pass active Filter (1st order) & study Frequency Response
10. Design a Butterworth High Pass active Filter (1st order) & study Frequency Response
11. Design a digital to analog converter (DAC) of given specifications.

Section-B: Digital circuits (Hardware)

1. (a) To design a combinational logic system for a specified Truth Table.
(b) To convert Boolean expression into logic circuit & design it using logic gate ICs.
(c) To minimize a given logic circuit.
2. Half Adder and Full Adder.
3. Half Subtractor and Full Subtractor.
4. 4 bit binary adder and adder-subtractor using Full adder IC.
5. To design a seven segment decoder.
6. To design an AstableMultivibrator of given specification using IC 555 Timer.
7. To design a MonostableMultivibrator of given specification using IC 555 Timer.
8. To build Flip-Flop (RS, Clocked RS, D-type and JK) circuits using NAND gates.
9. To build JK Master-slave flip-flop using Flip-Flop ICs
10. To build a Counter using D-type/JK Flip-Flop ICs and study timing diagram.
11. To make a Shift Register (serial-in and serial-out) using D-type/JK Flip-Flop ICs.

Section-C: SPICE/MULTISIM simulations for electronic circuits and devices

1. To verify the Thevenin and Norton Theorems.
2. Design and analyze the series and parallel LCR circuits
3. Design the inverting and non-inverting amplifier using an Op-Amp of given gain
4. Design and Verification of op-amp as integrator and differentiator
5. Design the 1st order active low pass and high pass filters of given cutoff frequency
6. Design a Wein's Bridge oscillator of given frequency.
7. Design clocked SR and JK Flip-Flop's using NAND Gates
8. Design 4-bit asynchronous counter using Flip-Flop ICs
9. Design the CE amplifier of a given gain and its frequency response.

Reference Books

1. Digital Principles and Applications, A.P. Malvino, D.P. Leach and Saha, 7th Ed., 2011, Tata McGraw
 2. OP-Amps and Linear Integrated Circuit, R. A. Gayakwad, 4th edn., 2000, Prentice Hall
 3. R. L. Tokheim, Digital Principles, Schaum's Outline Series, Tata McGraw- Hill (1994)
 4. Digital Electronics, S.K. Mandal, 2010, 1st edition, McGraw Hill
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